

Fig. 1 PRIOR ART

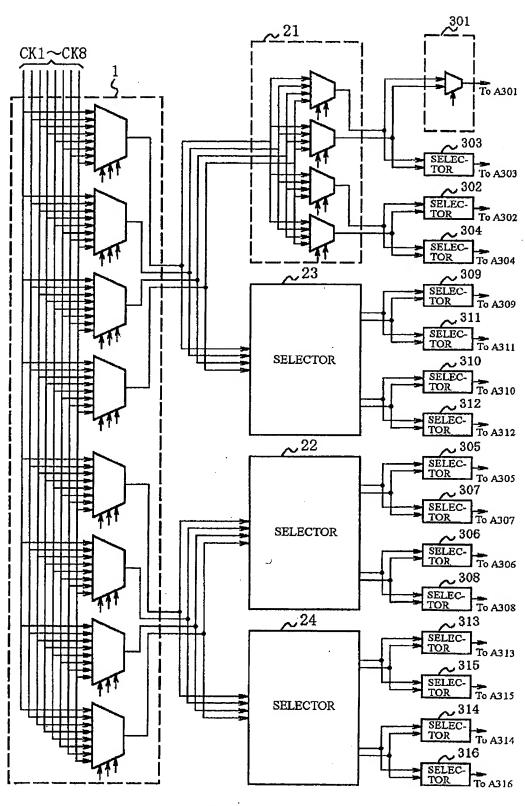


Fig. 2 PRIOR ART

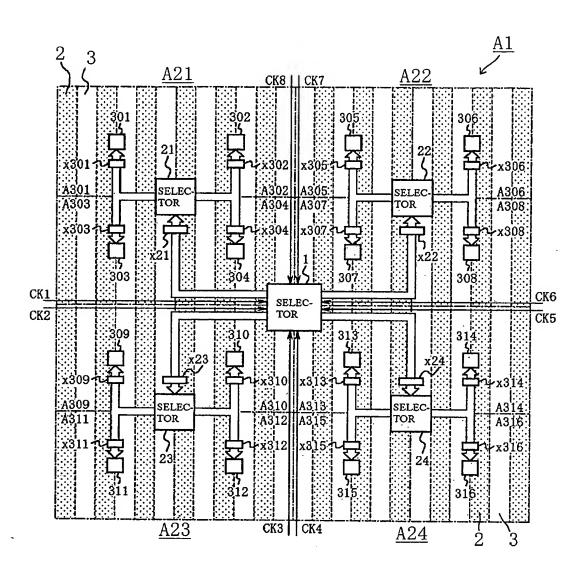


Fig. 3

Title: MASTER SLICE SEMICONDUCTOR INTEGRATED CIRCUIT

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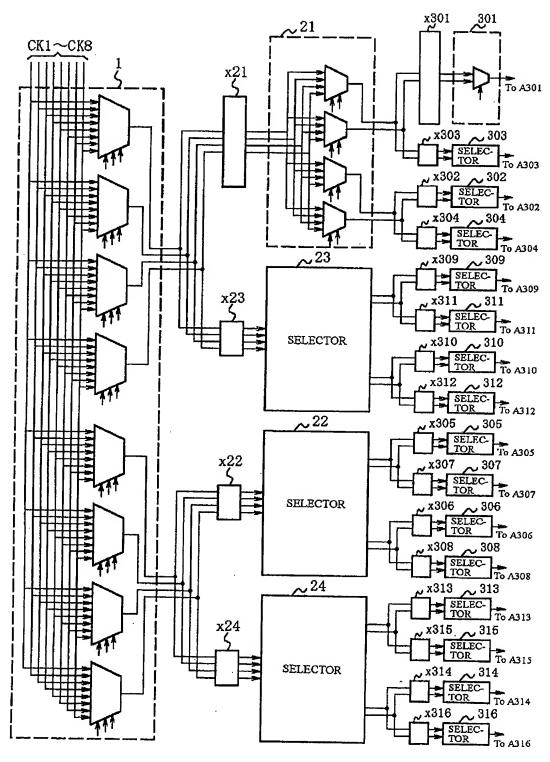


Fig. 4

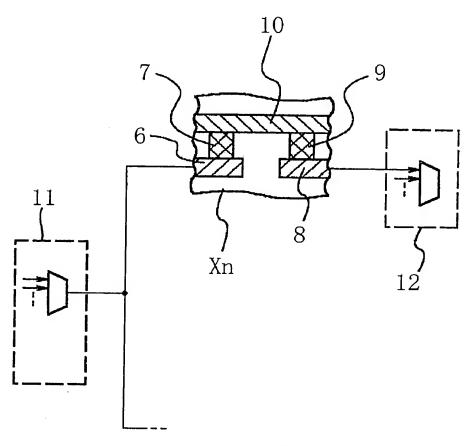
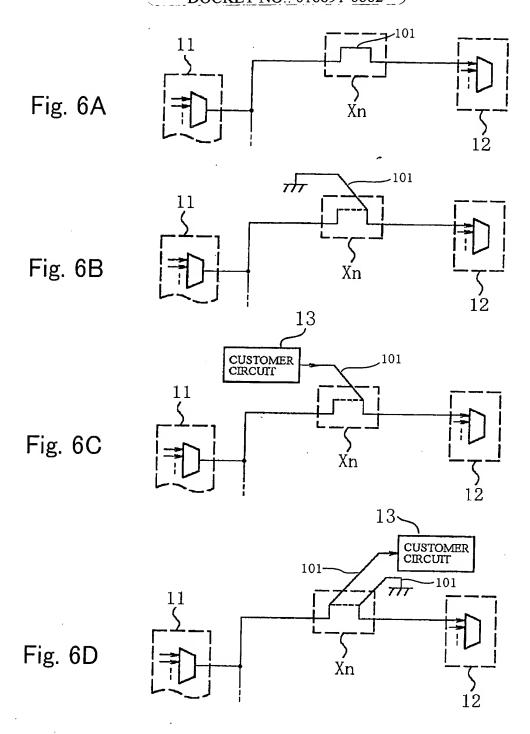
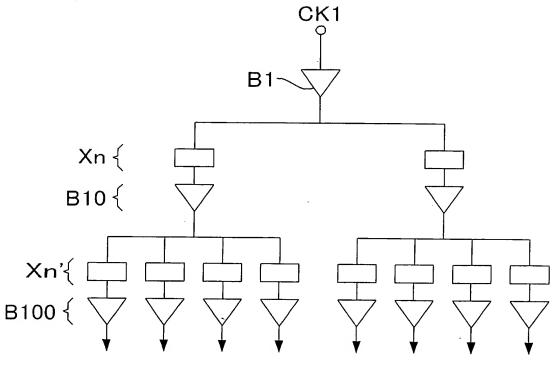


Fig. 5





To SEQUENTIAL CIRCUITS

Fig. 7